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	Filing Date		2004-05-20	
	First Named Inventor	Dominik Schmidt		
	Art Unit		2182	
	Examiner Name	Phan, Dean		
Attorney Docket Number		6057-61200		

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1	SAHULA, VINEET and C.P. Ravikumar, "Design Planning for Single Chip Implementation of Digital Wireless Mobile Transceiver, Paper, 2000, 2000 IEEE International Conference on Personal Wireless Communications, Conference Proceedings, IEEE, Piscataway, NJ, p. 19-23.	<input type="checkbox"/>
2	PARSSINEN, AARNO et al., "A Wide-Band Direct Conversion Receiver with On-Chip A/D Converters," Paper, 2000, 2000 Symposium on VLSI Circuits Digest of Technical Papers, IEEE, Piscataway, NJ, p. 32-33.	<input type="checkbox"/>
3	JIN, HENG and C. Andre T. Salama, "A 1-V, 1.9-GHz CDMA, CMOS on SOI, Low Noise Amplifier," Paper, 2000, 2000 IEEE International SOI Conference Proceedings, IEEE, Piscataway, NJ, p. 102-103.	<input type="checkbox"/>
4	SINGH, JUGDUTT, "Mixed-Signal Data Conversion Chip for Wireless Communication Systems," Paper, September 1999, WCNC. 1999 Wireless Communications and Networking Conference, IEEE, New Orleans, LA, p. 737-741.	<input type="checkbox"/>
5	RAEL, JACOB, Ahmadreza Rofougaran and Asad Abkdi et al., "Design Methodology Used in a Single-Chip CMOS 900 MHz Spread-Spectrum Wireless Transceiver," Paper, 1998, Proceedings 1998 Design and Automation Conference, IEEE, New York, NY, p. 44-49.	<input type="checkbox"/>
6	MEGAHED, MOHAMED et al., "UTSI(R) CMOS Technology for System-on-Chip Solution," Paper, 1998, 1998 Topical Meeting on Silicon Monolithic Integrated Circuits in RF Systems Digest of Papers, IEEE, Piscataway, NJ, p. 94-99.	<input type="checkbox"/>
7	KOSTIC, ZORAN and Selvaraj Seetharaman, "Digital Signal Processors in Cellular Radio Communications," Article, December 1997, IEEE Communications Magazine, p. 22-35.	<input type="checkbox"/>

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